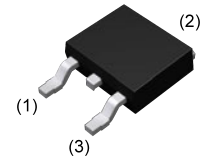


200V N-Channel Enhancement Mode MOSFET

Features

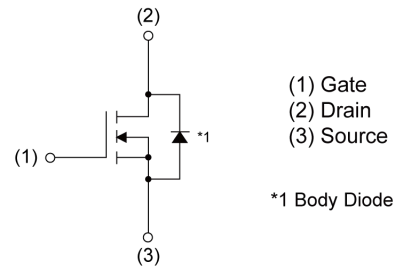
- Advanced Super Trench technology
- Low Gate Charge.
- Low On-Resistance
- Low Reverse transfer capacitances
- Fast Switching
- Reliable and Rugged
- Fully lead (Pb)-free device
- 100% avalanche energy Test



TO-252 View

Applications

- Power Management.
- PWM Application.
- Load Switching.



Schematic Diagram

Product Summary

Parameter	Value	Unit
V_{DS}	200	V
$I_D @ V_{GS}=10V$	90	A
$R_{DS(ON)}(typ.) @ V_{GS}=10V$	37	m Ω



Order information

Product Name	Package	Media	Q'ty (pcs)
XPX90N20FD	TO-252	Reel&Tape	2500

200V N-Channel Enhancement Mode MOSFET
Absolute maximum ratings (at $T_J = 25^\circ\text{C}$, unless otherwise specified)

Symbol	Parameter		Rating	Unit
V_{DS}	Drain-Source Voltage		200	V
V_{GS}	Gate-Source Voltage		± 20	V
I_D	Drain Current -Continuous ③	$T_C = 25^\circ\text{C}$	90	A
		$T_C = 100^\circ\text{C}$	50	A
I_{DM}	Drain Current -Pulsed ① ③	$T_C = 25^\circ\text{C}$	191	A
P_D	Maximum Power Dissipation	$T_C = 25^\circ\text{C}$	142	W
		$T_C = 100^\circ\text{C}$	71	W
R_{thJ-C}	Thermal Resistance-Junction to Case	Steady State	1.06	$^\circ\text{C}/\text{W}$
R_{thJ-A}	Thermal Resistance-Junction to Ambient	Steady State	50	$^\circ\text{C}/\text{W}$
I_{AS}	Avalanche Energy, Single pulse ② ④	$L=1\text{mH}$	18	A
E_{AS}	Avalanche Energy, Single pulse ② ④	$L=1\text{mH}$	162	mJ
T_{stg}	Storage Temperature		-55 to 175	$^\circ\text{C}$
T_J	Maximum Junction Temperature		175	$^\circ\text{C}$

Note :

- ①, Pulse width limited by maximum junction temperature.
- ②, UIS tested and pulse width limited by maximum junction temperature 175°C (initial temperature $T_J=25^\circ\text{C}$).
- ③, Current limited by bonding wire.
- ④, EAS Condition : $T_J=25^\circ\text{C}$, $V_D=100\text{V}$, $V_G=10\text{V}$, $L=1\text{mH}$, $R_g=25\Omega$.

200V N-Channel Enhancement Mode MOSFET
Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
Off Characteristics						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	200	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =200V, V _{GS} =0V, T _J =25°C	-	-	1	μA
		V _{DS} =200V, V _{GS} =0V, T _J =85°C	-	-	30	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	2.5	3.5	4.5	V
R _{DS(on)}	Drain-Source On-State Resistance (1)	V _{GS} =10V, I _D =15A	-	37	50	mΩ
Dynamic Characteristics(2)						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	2970	-	PF
C _{oss}	Output Capacitance		-	350	-	PF
C _{rss}	Reverse Transfer Capacitance		-	82	-	PF
t _{d(on)}	Turn-on Delay Time	V _{DD} =100V, I _D =1A, V _{GEN} =10V, R _G =3.0Ω.	-	20	-	nS
t _r	Turn-on Rise Time		-	8.5	-	nS
t _{d(off)}	Turn-Off Delay Time		-	34	-	nS
t _f	Turn-Off Fall Time		-	11	-	nS
Q _g	Total Gate Charge		-	60	-	nC
Q _{gs}	Gate-Source Charge	V _{DS} =100V, I _D =15A, V _{GS} =10V.	-	18	-	nC
Q _{gd}	Gate-Drain Charge		-	12	-	nC
Drain-Source Diode Characteristics						
I _S	Maximun Body-Diode Continuous Current		-	53	-	A
I _{SM}	Maximun Body-Diode Pulsed Current		-	191	-	A
V _{SD}	Diode Forward Voltage(1)	V _{GS} =0V, I _S =1A	-	0.8	1.3	V
t _{rr}	Reverse Recovery Time	I _S =15A, dI _S /dt=100A/μs	-	120	-	ns
Q _{rr}	Reverse Recovery Charge		-	360	-	nC

Note:

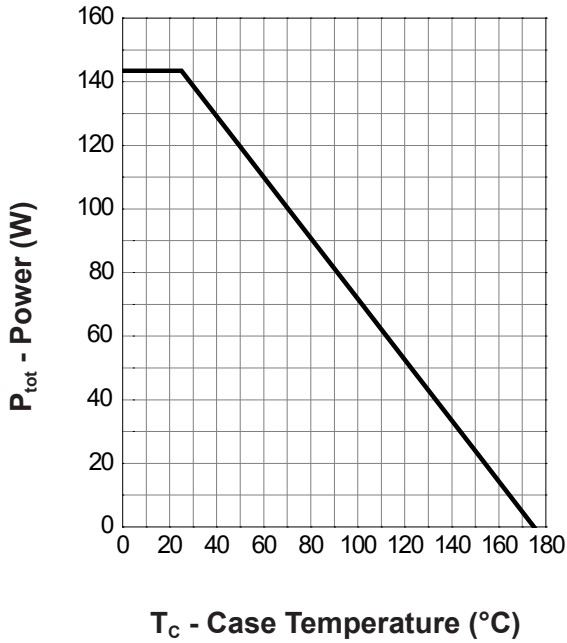
(1): Pulse test ; pulse width ≤ 300μs, duty cycle ≤ 2%.

(2): Guaranteed by design, not subject to production testing.

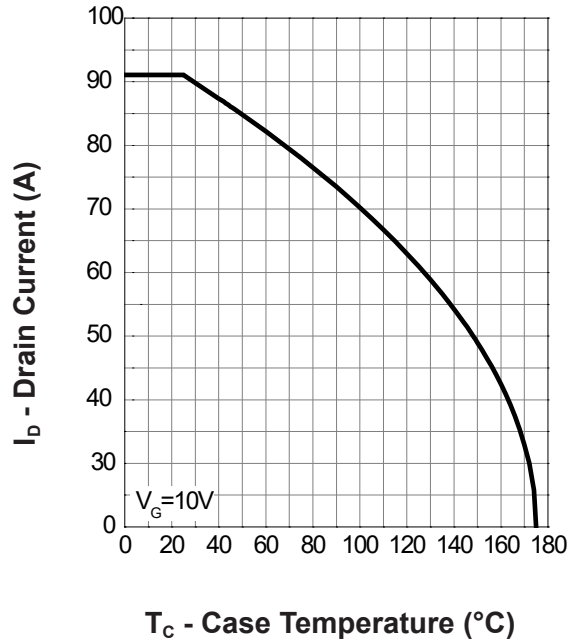
200V N-Channel Enhancement Mode MOSFET

Typical Operating Characteristics

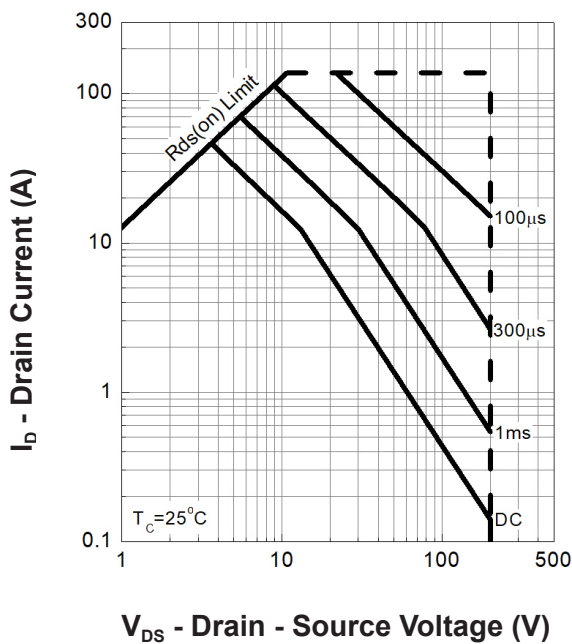
Power Dissipation



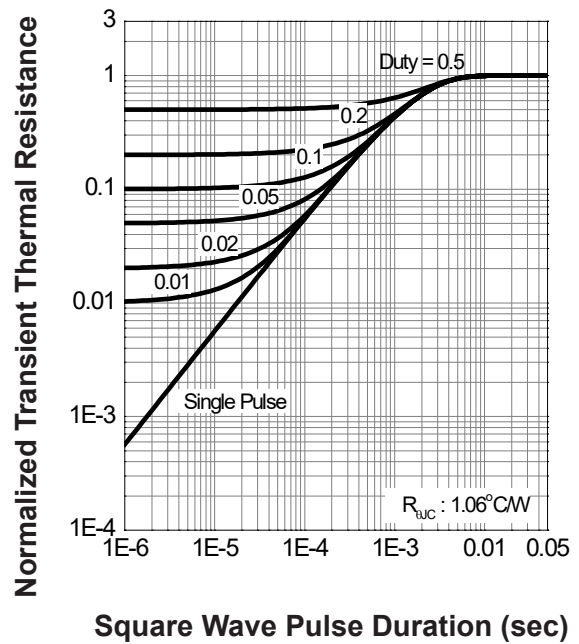
Drain Current



Safe Operation Area



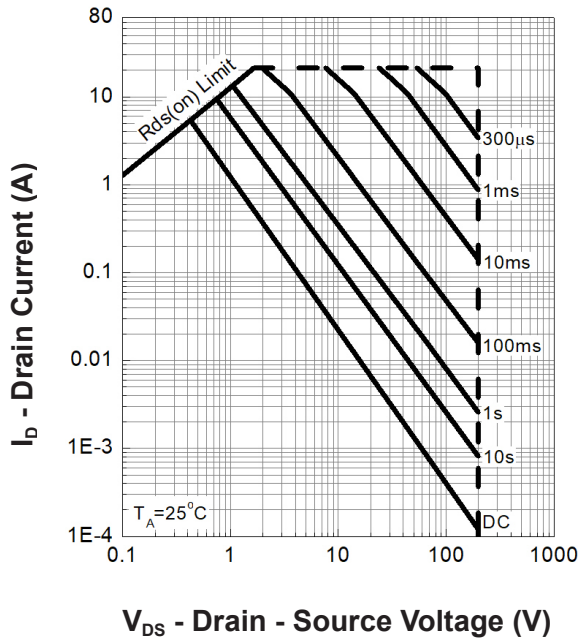
Thermal Transient Impedance



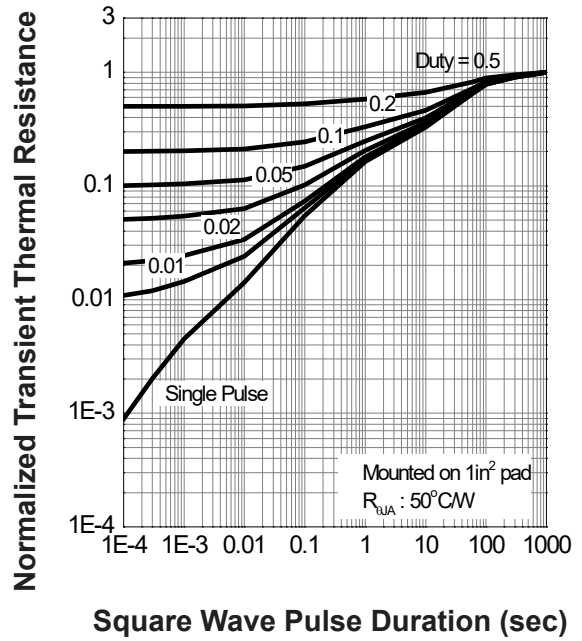
200V N-Channel Enhancement Mode MOSFET

Typical Operating Characteristics

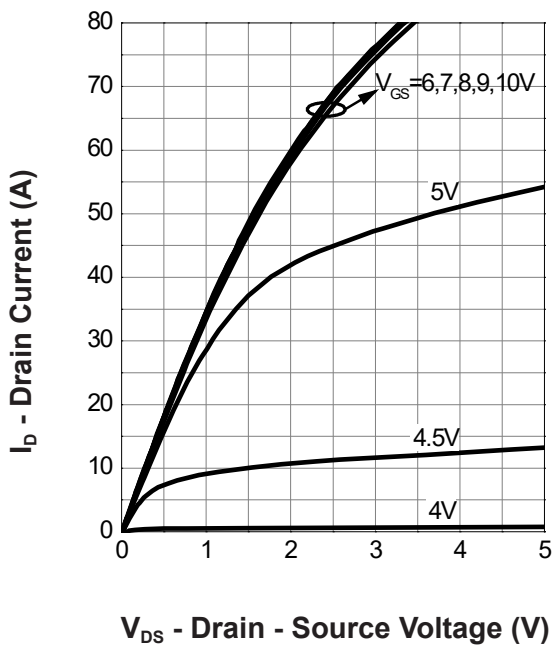
Safe Operation Area



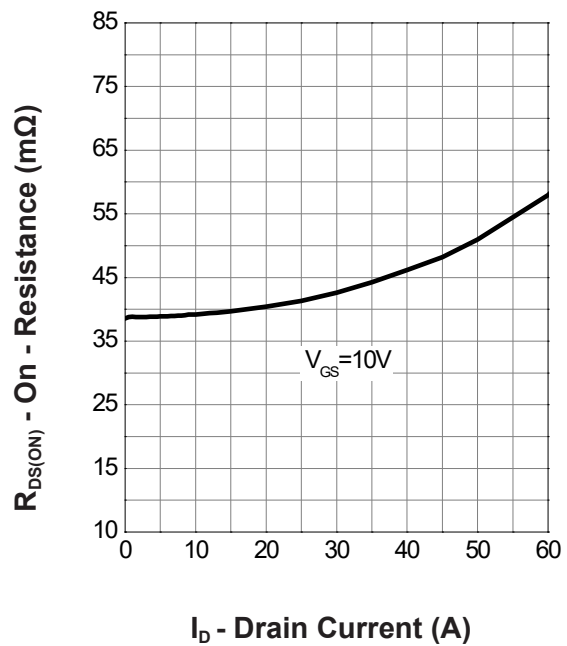
Thermal Transient Impedance



Output Characteristics



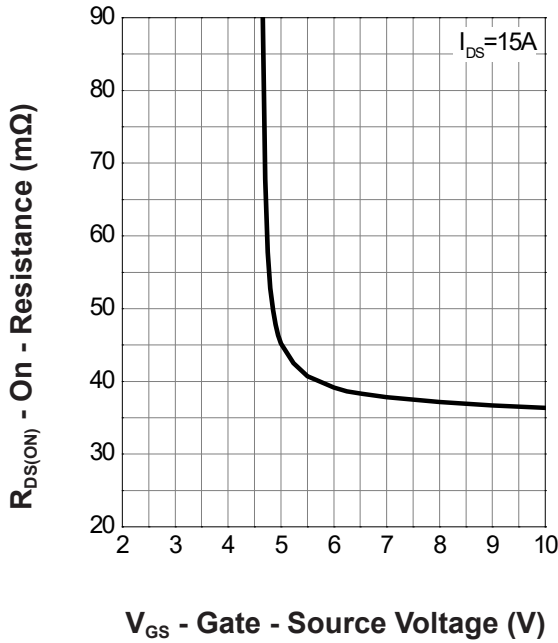
Drain-Source On Resistance



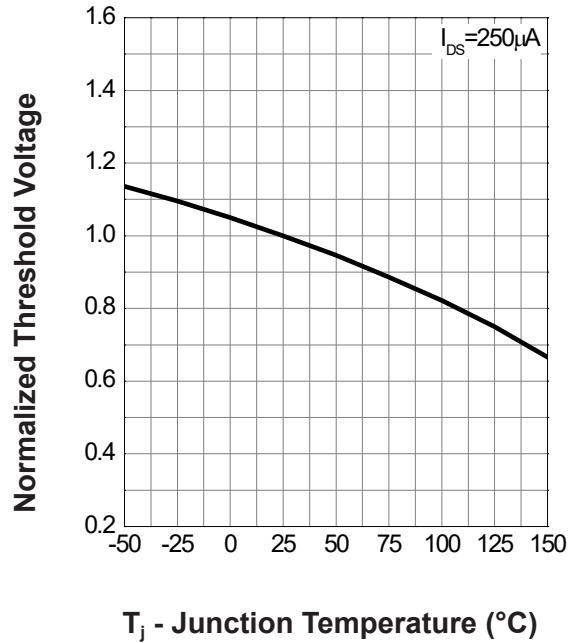
200V N-Channel Enhancement Mode MOSFET

Typical Operating Characteristics

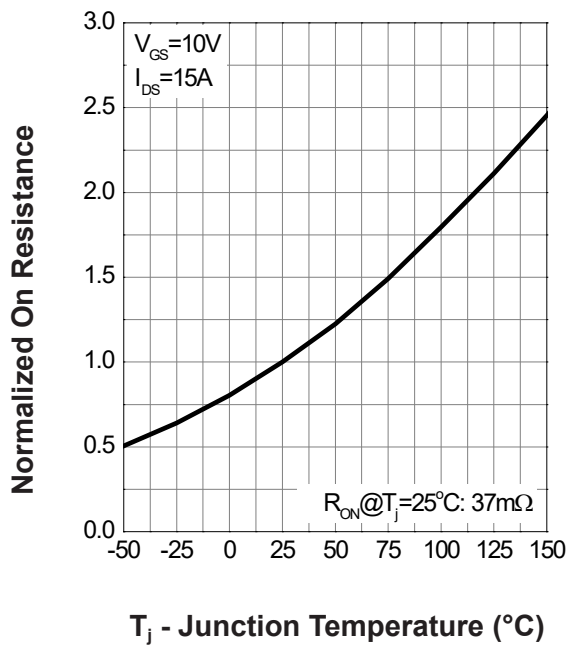
Gate-Source On Resistance



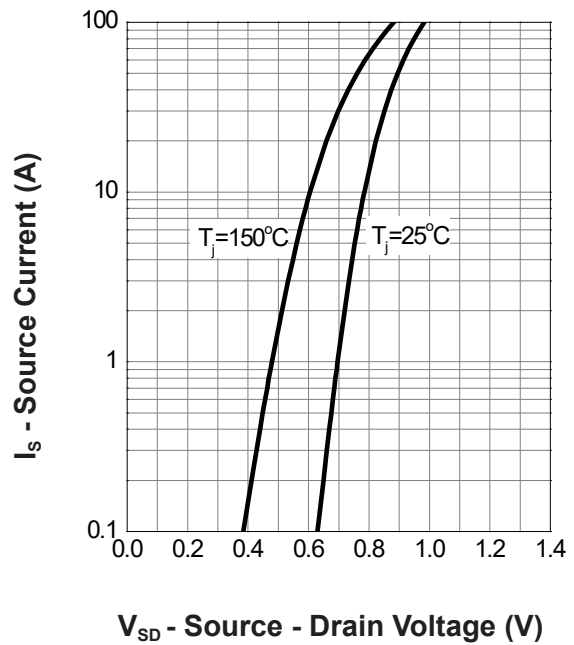
Gate Threshold Voltage



Drain-Source On Resistance



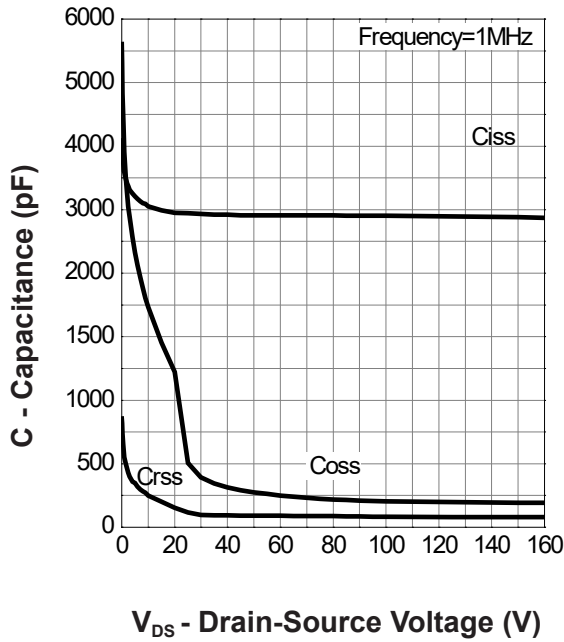
Source-Drain Diode Forward



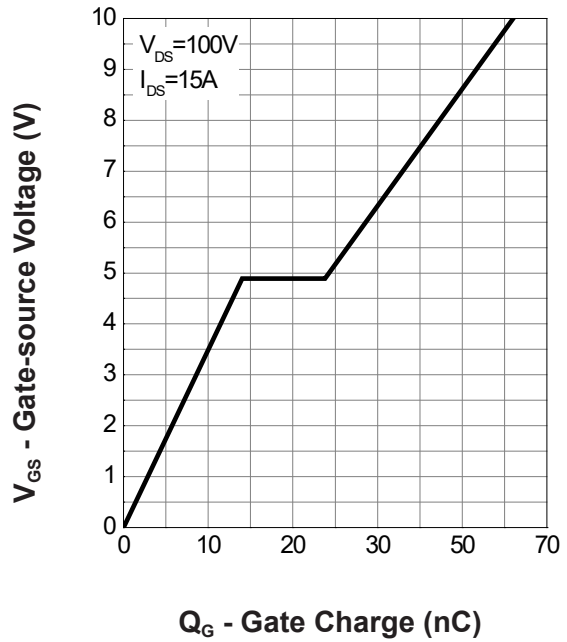
200V N-Channel Enhancement Mode MOSFET

Typical Operating Characteristics

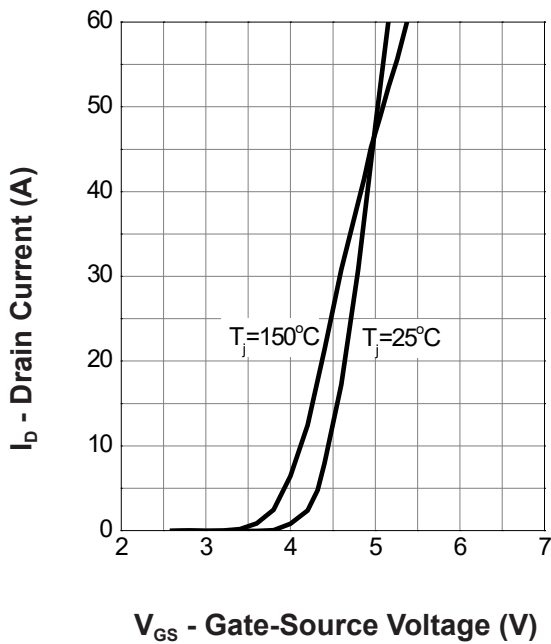
Capacitance

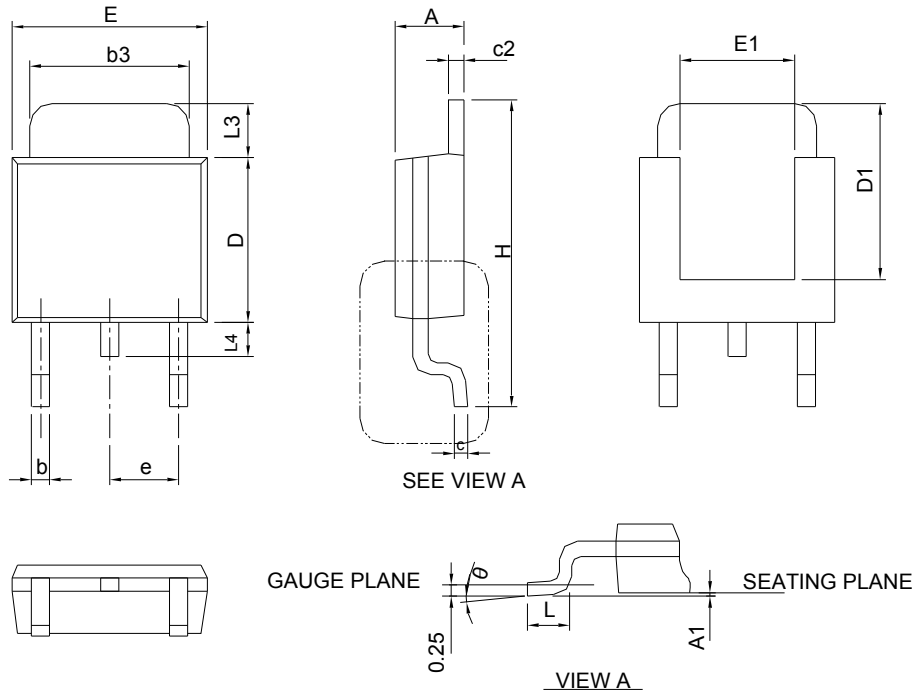


Gate Charge

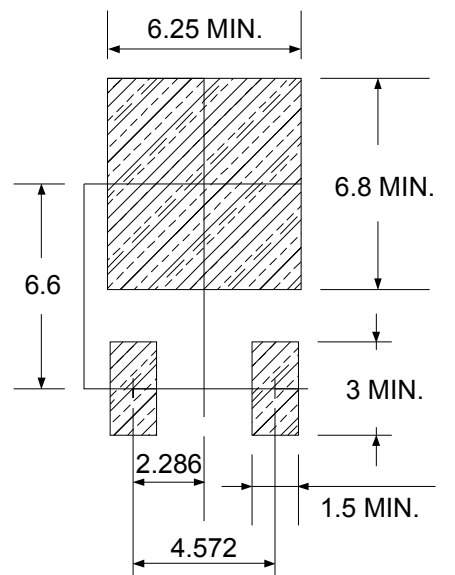


Transfer Characteristics



200V N-Channel Enhancement Mode MOSFET
Package Information: TO-252


DIMENSIONS	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
	A	2.18	2.39	0.086
A1	-	0.13	-	0.005
b	0.50	0.89	0.020	0.035
b3	4.95	5.46	0.195	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	2.29 BSC		0.090 BSC	
H	9.40	10.41	0.370	0.410
L	0.90	1.78	0.035	0.070
L3	0.89	2.03	0.035	0.080
L4	-	1.02	-	0.040
θ	0°	8°	0°	8°

RECOMMENDED LAND PATTERN


UNIT: mm

200V N-Channel Enhancement Mode MOSFET

Flow (wave) soldering (solder dipping)

Product	Peak Temperature	Dipping Time
Pb device	245°C ±5°C	5sec±1 sec
Pb-Free device	260°C +0/-5°C	5sec±1 sec



This integrated circuit can be damaged by ESD. UniverChip Corporation recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedure can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

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